

## B\_S-2W/ B\_D-2W Series

FIXED INPUT ISOLATED & UNREGULATED  
2W OUTPUT SINGLE OUTPUT  
MINIATURE SIP(DIP) PACKAGE

multi-country patent protection

### FEATURES

- Efficiency up to 86%
- Miniature SIP or DIP Package
- Single Output Voltage
- 1KVDC Isolation
- Power Density up to 1.40W/cm<sup>3</sup>
- Fully Encapsulated
- Temperature Range: -40°C~+85°C
- Industry Standard Pinout
- UL94-V0 Package
- No Heat sink Required
- No External Component Required
- PCB Mounting
- Footprint From 1.05cm<sup>2</sup>
- RoHS Compliance

### APPLICATIONS

The B\_S(D)-2W Series is specially designed for applications where a single power supply is isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) Where the voltage of the input power supply is fixed (voltage variation  $\leq \pm 10\%$ );
- 2) Where isolation is necessary between input and output (isolation voltage = 1000VDC);
- 3) Where the regulation of the output voltage and the output ripple noise are not demanding.

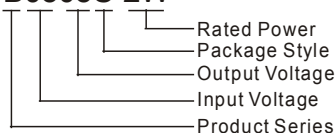
Such as: purely digital circuits, ordinary low frequency analog circuits.

These products don't apply to:

- 1) Where the input supply voltage is varied (variation  $\geq \pm 10\%$ ), otherwise our company's WRB series is recommended;
- 2) Where the isolation voltage between input and output is required to be >1000VDC, otherwise our company's F\_S(D)-2W Series of products are recommended;
- 3) The output load's actual power consumption is less than 0.25W, otherwise our company's B\_LS(D)-1W Series are recommended.

### MODEL SELECTION

**B0505S-2W**



PRODUCT PROGRAM							
Part Number	Input		Output			Efficiency (% , Typ)	Package Style
	Voltage (VDC)		Voltage (VDC)	Current (mA)			
	Nominal	Range		Max	Min		
B0505S/D-2W	5	4.5~5.5	5	400	40	81	SIP/DIP
B0509S/D-2W	5	4.5~5.5	9	222	23	82	SIP/DIP
B0512S/D-2W	5	4.5~5.5	12	167	17	84	SIP/DIP
B0515S/D-2W	5	4.5~5.5	15	133	14	84	SIP/DIP
B1205S/D-2W	12	10.8~13.2	5	400	40	82	SIP/DIP
B1209S/D-2W	12	10.8~13.2	9	222	23	83	SIP/DIP
B1212S/D-2W	12	10.8~13.2	12	167	17	85	SIP/DIP
B1215S/D-2W	12	10.8~13.2	15	133	14	85	SIP/DIP
B2405S/D-2W	24	21.6~26.4	5	400	40	83	SIP/DIP
B2409S/D-2W	24	21.6~26.4	9	222	23	84	SIP/DIP
B2412S/D-2W	24	21.6~26.4	12	167	17	86	SIP/DIP
B2415S/D-2W	24	21.6~26.4	15	133	14	86	SIP/DIP
B2424S/D-2W	24	21.6~26.4	24	83	8	86	SIP/DIP

COMMON SPECIFICATIONS	
Short circuit protection	1second
Temperature rise at full load	25°C MAX, 15°C TYP
Cooling	Free air convection
No load power consumption	10% nominal power (typical)
Operating temperature range	-40°C~+85°C
Storage temperature range	-55°C ~+125°C
Lead temperature*	300°C
Storage humidity range	≤95%
Case material	Plastic (UL94-V0)
MTBF	>3,500,000 hours
*Lead temperature 1.5mm from case for 10 seconds.	

ISOLATION SPECIFICATIONS					
Item	Test conditions	Min	Typ	Max	Units
Isolation voltage	Tested for 1 minute	1000			VDC
Isolation resistance	Test at 500VDC	1000			MΩ

OUTPUT SPECIFICATIONS					
Item	Test condition	Min	Typ	Max	Units
Output power		0.2		2	W
Line regulation	For Vin change of 1%			1.2	%
Load regulation	10% to 100%		10	15	%
Output voltage accuracy	See tolerance envelope graph				
Temperature drift	100% full load			0.03	%/°C
Output ripple	20MHz Bandwidth		100	150	mVp-p
Switching frequency	Full load, nominal input		75		KHz

Note:

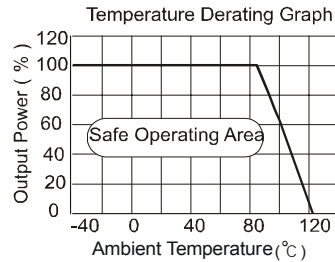
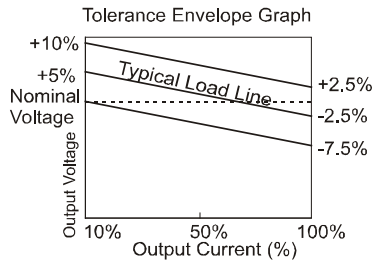
1. All specifications measured at TA=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
2. See below recommended circuits for more details.



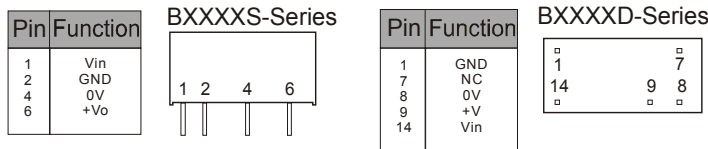
**MORNSUN Science & Technology Ltd.**

Address: 8th floor 8th building, Hangzhou Industrial District, Guangzhou, China  
Tel: 86-20-38601850  
Fax: 86-20-38601272  
http://www.mornsun.cn

## TYPICAL CHARACTERISTICS

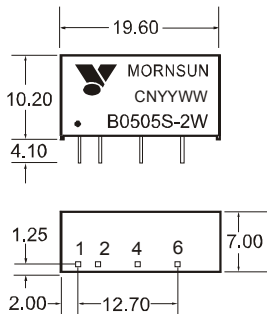


## PIN CONNECTIONS

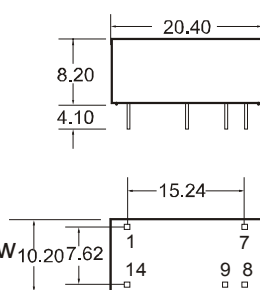


## OUTLINE DIMENSIONS & RECOMMENDED FOOTPRINT DETAILS

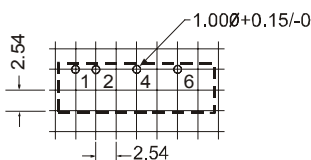
### BXXXXS-2W Package



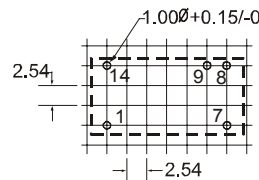
### BXXXXD-2W Package



### BXXXXS-2W Package



### BXXXXD-2W Package



Note: All Pins on a 2.54mm pitch; all pin diameters are 0.50mm; all dimensions in mm.

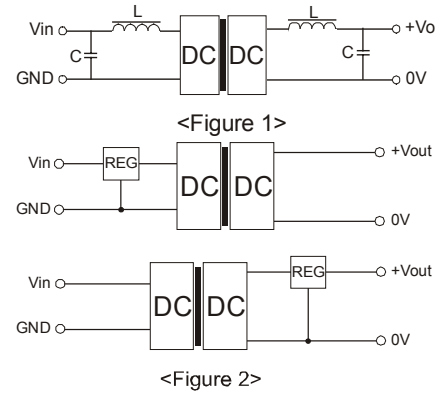
## APPLICATION NOTE

### Filtering

In some circuits which are sensitive to noise and ripple, a filtering capacitor may be added to the DC/DC output end and input end to reduce the noise and ripple. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees the external capacitor table. To get an extremely low ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, which may produce a more significant filtering effect. It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference (see figure 1).

### Requirement on output load

To ensure this module can operate efficiently and reliably, a minimum load is specified for this kind of DC/DC converter in addition to a maximum load (namely full load). During operation, make sure the specified range of input voltage is not exceeded, the minimum output load is **not less than 10%** of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power (B\_LS(D) -1W series).



### Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

### Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (see Figure 2).

### External Capacitor Table

V <sub>in</sub>	External capacitor	V <sub>out</sub>	External capacitor
5VDC	4.7 $\mu$ F	5VDC	10 $\mu$ F
12VDC	2.2 $\mu$ F	9VDC	4.7 $\mu$ F
24VDC	1 $\mu$ F	12VDC	2.2 $\mu$ F
--	--	15VDC	1 $\mu$ F



**MORNSUN Science & Technology Ltd.**

Address: 8th floor 8th building, Huangzhou Industrial District, Guangzhou, China  
 Tel: 86-20-38601850  
 Fax: 86-20-38601272  
 Http: //www.mornsun.cn